| Notice of References Cited | Application/Control No. 10/683,925 | Applicant(s)/Patent Under Reexamination COURTAY, ALAN | |
|---|---|---|---|
| | Examiner Russ Guill | Art Unit 2123 | Page 1 of 1 |

**U.S. PATENT DOCUMENTS**

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|---|---|---|---|
| * | A | US-2002/0167544 | 11-2002 | Raghavan et al. | 345/763 |
| | B | US- | | | |
| | C | US- | | | |
| | D | US- | | | |
| | E | US- | | | |
| | F | US- | | | |
| | G | US- | | | |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

**FOREIGN PATENT DOCUMENTS**

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|---|---|---|---|---|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

**NON-PATENT DOCUMENTS**

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|---|
| | U | Panos J. Antsaklis, "Special Issue on Hybrid Systems: Theory and Application A Brief Introduction to the Theory and Applications of Hybrid Systems", July 200, Proceedings of the IEEE, Volume 88, Number 7, pages 879 - 887 |
| | V | Ernst Christen et al.; "VHDL-AMS - A Hardware Description Language for Analog and Mixed-Signal Applications", October 1999, IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Volume 46, Number 10, pages 1263 - 1272 |
| | W | Tom J. Kazmierski; "Fuzzy-logic digita-analogue interfaces for accurate mixed-signal simulation", 1998, Proceedings of the conference on Design, automation and test in Europe, pages: 941 - 944 |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.